DATAWEEK

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RISC-V: an opportunity for the European R&I&D ecosystems

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Overview of the session’s agenda

- 17:00-17:05 Jeanette Nilsson, RISE Welcome and scope of the session
- 17:05-17:25, Rob Wullems, NXP, Tristan project speech
- 17:25-17:45 Holger Schmidt, infineon, Isolde project speech
- 17:45-18:00 Valerio Frascolla, Intel Corporation Panel Discussion with Rob Wullems, Holger Schmidt, and Daniel Opalka

Panel questions

1. There are several initiatives to strenghten EU souvereineity on processing technologies, can you elaborate on them (EPI, RISC-V initiatives, etc.)?
2. What are the specificities of RISC-V architectures and why are those important for the EU?
3. EU supply chain: what is the importance of RISC-V architectures?
4. European collaboration with partners outside of the EU, can you elaborate on that?
5. New generation of algorithms to run on the next generation of processing elements: your view?
6. What do we need to strenghten SME competencies in HPC?

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Thank you!

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THE DATA IS OUT THERE